

FIG. 1

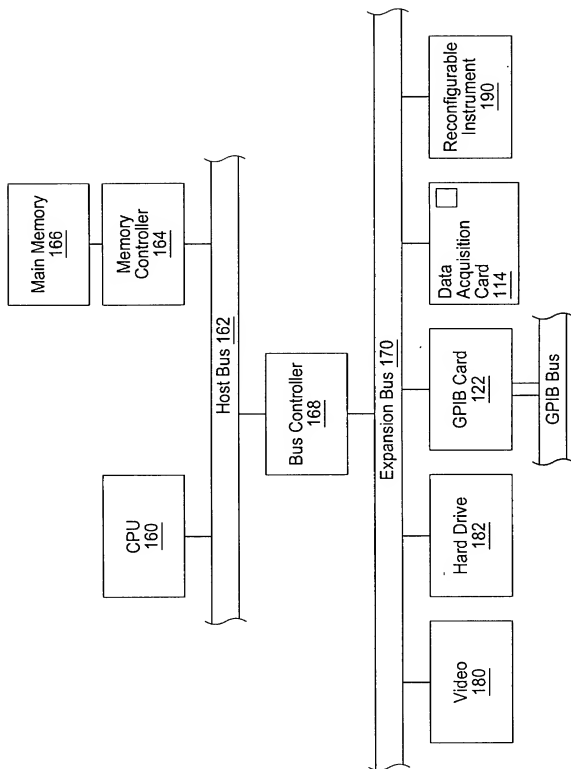


FIG. 2

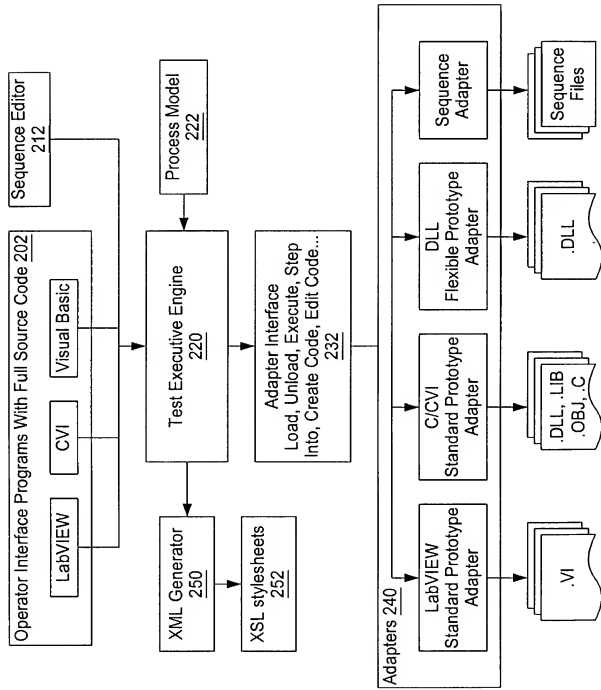


FIG. 3

C:\TestStand\Examples\WinLReports\computer.seq			
Main   Setup   Cleanup   Parameters   Locals   View: MainSequence			
Step	Description	Execution Flow	Comment
1 Power On			
2 CPU	Numeric Limit Test, 12 <= x <= 15, vch, Call P...	Precondition	
3 ROM	Call MainSequence in cpu.seq	Precondition	
4 RAM	Pass/Fail Test, Call ROMTest (computer.dtl)	Precondition	
5 Video	Pass/Fail Test, Call RAMTest (computer.dtl)	Precondition	
6 Keyboard	Numeric Limit Test, 70 <= x <= 75, hertz, Call ...	Precondition	
7 ROM Diagnostics	Pass/Fail Test, Call KeyboardTest (computer.dtl)	Precondition	
8 RAM Diagnostics	Action, Call ROMDiagnostics (computer.dtl)	Precondition	
9 Video Diagnostics	Action, Call RAMDiagnostics (computer.dtl)	Precondition	
10 Keyboard Diagnostics	Action, Call VideoDiagnostics (computer.dtl)	Precondition	
11 Keyboard Diagnostics	Action, Call KeyboardDiagnostics (computer.dtl)	Precondition	
END			

No Steps Selected Number of Steps: 10

FIG. 4



FIG. 5

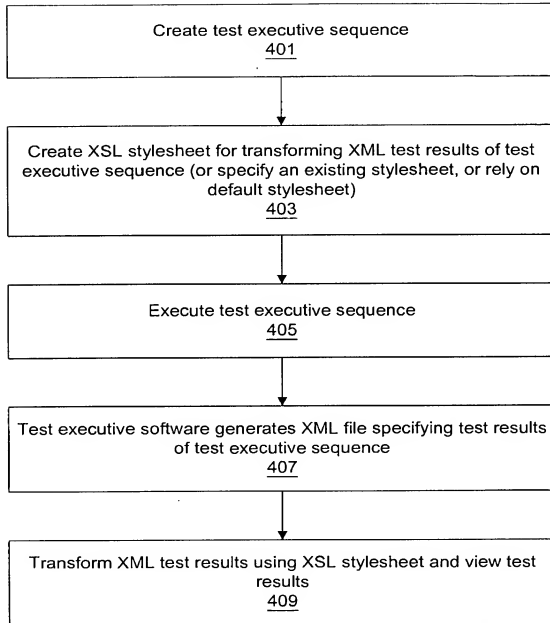


FIG. 6

## UUT Report

- Station ID: TCASTRO
- Serial Number: NONE
- Socket Index: 1
- Date: Friday, February 09, 2001
- Time: 3:21:59 PM
- Operator: administrator
- Number of Results: 18
- UUT Result: Failed
- Failure Chain:

Step	Operation	Sample Data
RAM	MainSequence	computer.seq

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Begin Sequence: MainSequence

(C:\TestStand\Examples\XMLReports\computer.seq)

Check If Run From Main Sequence	
Status:	Skipped
Explanation	
Status:	Skipped
Pre-test	
Status:	Done
Power On	
Status:	Failed
Numeric:	15
Units:	volt
Limits	
Low:	12
High:	13
Comp:	GELE (> <=)
CPU	
Status:	Failed

FIG. 7B



Begin Sequence: MainSequence  
 (C:\TestStand\Examples\XML\Reports\cpu.seq)

<b>Pre-test</b>	
Status:	Done
<b>Register</b>	
Status:	Pass
Pass/Fail:	True
<b>Instruction Set</b>	
Status:	Pass
Pass/Fail:	True
<b>Cache</b>	
Status:	Pass
Pass/Fail:	True
<b>FPU</b>	
Status:	Pass
Pass/Fail:	True

End Sequence: MainSequence

FIG. 7C

<b>ROM</b>	
Status:	True
PassFail:	True
<b>RAM</b>	
Status:	
PassFail:	False
<b>Video</b>	
Status:	True
Numeric:	75
Units:	hertz
Limits:	
Low:	70
High:	75
Comp:	GELE (>=<=<=)
<b>Keyboard</b>	
Status:	True
PassFail:	True
<b>ROM Diagnostics</b>	
Status:	Skipped
<b>RAM Diagnostics</b>	
Status:	Done
<b>Video Diagnostics</b>	
Status:	Skipped
<b>Keyboard Diagnostics</b>	
Status:	Skipped

End Sequence: MainSequence

End UUT Report

## UUT Report

- Station ID: TCASTRO
  - Serial Number: NONE
  - Socket Index: 0
  - Date: Friday, February 09, 2001
  - Time: 3:21:59 PM
  - Operator: administrator
  - Number of Results: 18
  - UUT Result: Passed
- 
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Begin Sequence: MainSequence  
 (C:\TestStand\Examples\XMLReports\computer.seq)

Check If Run From Main Sequence	
Status:	Skipped
Explanation:	
Status:	Skipped
Pre-Test:	
Status:	Done
Power On:	
Status:	On
Numeric:	15
Units:	volt
Limits:	
Low	12
High	15
Comp:	GELE ( $\frac{15}{12} < 1$ )
CPU:	
Status:	On

FIG. 7F

Begin Sequence: MainSequence  
 (C:\TestStand\Examples\XMLReports\cpu.seq)

Promote	
Status:	Pass
Register	
Status:	Pass
Pass/Fail:	True
Instruction Set	
Status:	Pass
Pass/Fail:	True
Cache	
Status:	Pass
Pass/Fail:	True
FPU	
Status:	Pass
Pass/Fail:	True

End Sequence: MainSequence

FIG. 7G

<b>ROM</b>	
Status:	Pass
PassFail:	True
<b>RAM</b>	
Status:	Pass
PassFail:	True
<b>Video</b>	
Status:	Pass
Numeric:	75
Units:	hertz
Limits:	
Low:	70
High:	75
Comp:	GELE(2--2)
<b>Keyboard</b>	
Status:	Pass
PassFail:	True
<b>ROM Diagnostics</b>	
Status:	Skipped
<b>RAM Diagnostics</b>	
Status:	Skipped
<b>Video Diagnostics</b>	
Status:	Skipped
<b>Keyboard Diagnostics</b>	
Status:	Skipped

End Sequence: MainSequence

**End UUT Report**

FIG. 7H

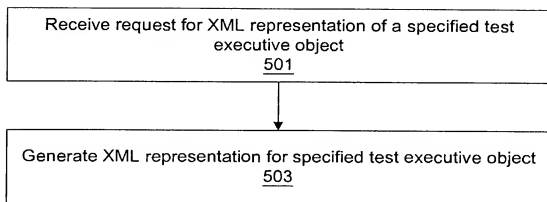


FIG. 8

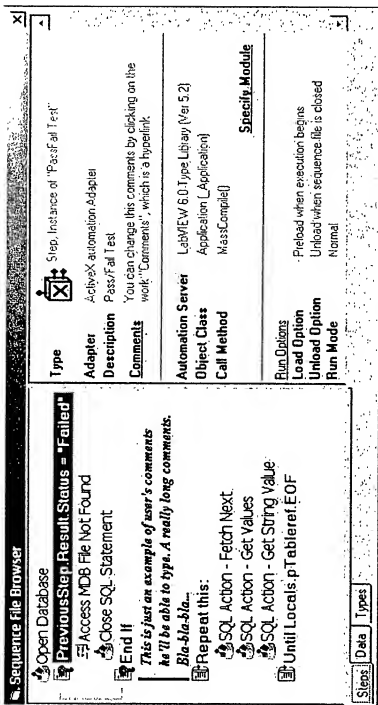


FIG. 9